

ABSTRACT

A passive component (10A) comprising one input electrode layer (48) constituting an input terminal, one output electrode layer (50) constituting an output terminal, and four shield electrode layers (52a-52d) constituting shield terminals, all formed in the lowermost dielectric layer (S7) by via holes. The input electrode layer (48) is connected electrically with an input-side resonance electrode (26), in the vicinity of the second side face (14b) of a dielectric substrate (14), through a via hole (54) made in the fourth through sixth dielectric layers (S4-S6) and an input tap electrode (30). The output electrode layer (50) is connected electrically with an output-side resonance electrode (28), in the vicinity of the third side face (14c) of the dielectric substrate (14), through a via hole (56) made in the fourth through sixth dielectric layers (S4-S6) and an output tap electrode (32).